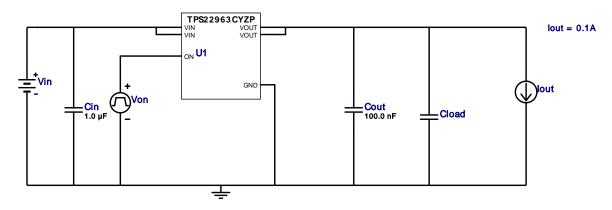


WEBENCH® Design Report

VinMin = 5.0V VinMax = 5.0V Vout = 5.0V lout = 0.1A Device = TPS22963CYZPR Topology = Load Switch Created = 8/21/16 8:51:45 AM BOM Cost = \$0.31 BOM Count = 4 Total Pd = 0.0W

Design: 4009018/126 TPS22963CYZPR

Design 126 - TPS22963CYZPR



1. To limit the voltage drop on the input supply caused by transient in-rush currents when the switch turns on into a discharged load capacitor or a short circuit, it is generally recomended to have a capacitor of at least Cload*10 between VIN and GND.

Electrical BOM

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cin	Kemet	C0603C105Z9VACTU Series= Y5V	Cap= 1.0 uF VDC= 6.3 V IRMS= 0.0 A	1	\$0.01	0603 5 mm ²
2.	Cout	MuRata	GRM155R60J104KA01D Series= X5R	Cap= 100.0 nF VDC= 6.3 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
3.	Cout	MuRata	GRM155R60J104KA01D Series= X5R	Cap= 100.0 nF VDC= 6.3 V IRMS= 0.0 A	1	\$0.01	0402 3 mm ²
4.	U1	Texas Instruments	TPS22963CYZPR	Switcher	1	\$0.28	YZP0006AFAF 5 mm²

Operating Values

#	Name	Value	Category	Description
1.	BOM Count	4	General	Total Design BOM count
2.	FootPrint	15.0 mm ²	General	Total Foot Print Area of BOM components
3.	Inrush Current	200.0 mA	General	User entered Inrush Current
4.	Pout	499.87 mW	General	Total output power
5.	Total BOM	\$0.31	General	Total BOM Cost
	1. 2. 3. 4.	 BOM Count FootPrint Inrush Current Pout 	1. BOM Count 4 2. FootPrint 15.0 mm² 3. Inrush Current 200.0 mA 4. Pout 499.87 mW	1. BOM Count 4 General 2. FootPrint 15.0 mm² General 3. Inrush Current 200.0 mA General 4. Pout 499.87 mW General

#	Name	Value	Category	Description
6.	Cload Act	200.0 nF	Op_Point	Cload (Actual)
7.	Ron Act	12.954 mOhm	Op_Point	Ron (Actual)
8.	SlewRate Act	4.305 mV/us	Op_Point	Change in volt per unit time
9.	Trise Act	928.996 µs	Op_Point	Rise time
10.	Vdrop Act	1.295 mV	Op_Point	Voltage drop
11.	DC Load Fall Time	21.967 µs	Op_point	Fall time calculated with the DC load attached. Considering only CLoad
				+ Cout and RLoad
12.	DC Load Inrush Curre	ent80.861 mA	Op_point	Inrush current calculated with the DC load connected
13.	Efficiency	99.974 %	Op_point	Steady state efficiency
14.	IC Tj	30.061 degC	Op_point	IC junction temperature
15.	IOUT_OP	100.0 mA	Op_point	lout operating point
16.	No Load Fall Time	NaN s	Op_point	
17.	No Load Inrush Curre	ent 80.861 mA	Op_point	Inrush current calculated with the DC load not connected
18.	VIN_OP	5.0 V	Op_point	Vin operating point
19.	Total Pd	465.508 µW	Power	Total Power Dissipation

Design Inputs

	5 1		
#	Name	Value	Description
1.	lout	100.0 m	Maximum Output Current
2.	lout	100.0 m	Maximum Output Current
3.	VinMax	5.0	Maximum input voltage
4.	VinMin	5.0	Minimum input voltage
5.	base_pn	TPS22963C	Texas Instruments Base Part Number
6.	cload	100.0 n	Minimum load capacitance user requirement
7.	inrush_Current	200.0 m	Inrush current
8.	source	DC	Input Source Type
9.	ta	30.0	Ambient temperature
10.	vdrop_max	200.0 m	Maximum voltage drop user requirement

Design Assistance

1. TPS22963C Product Folder: http://www.ti.com/product/tps22963c: contains the data sheet and other resources.

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